

Fig. 1

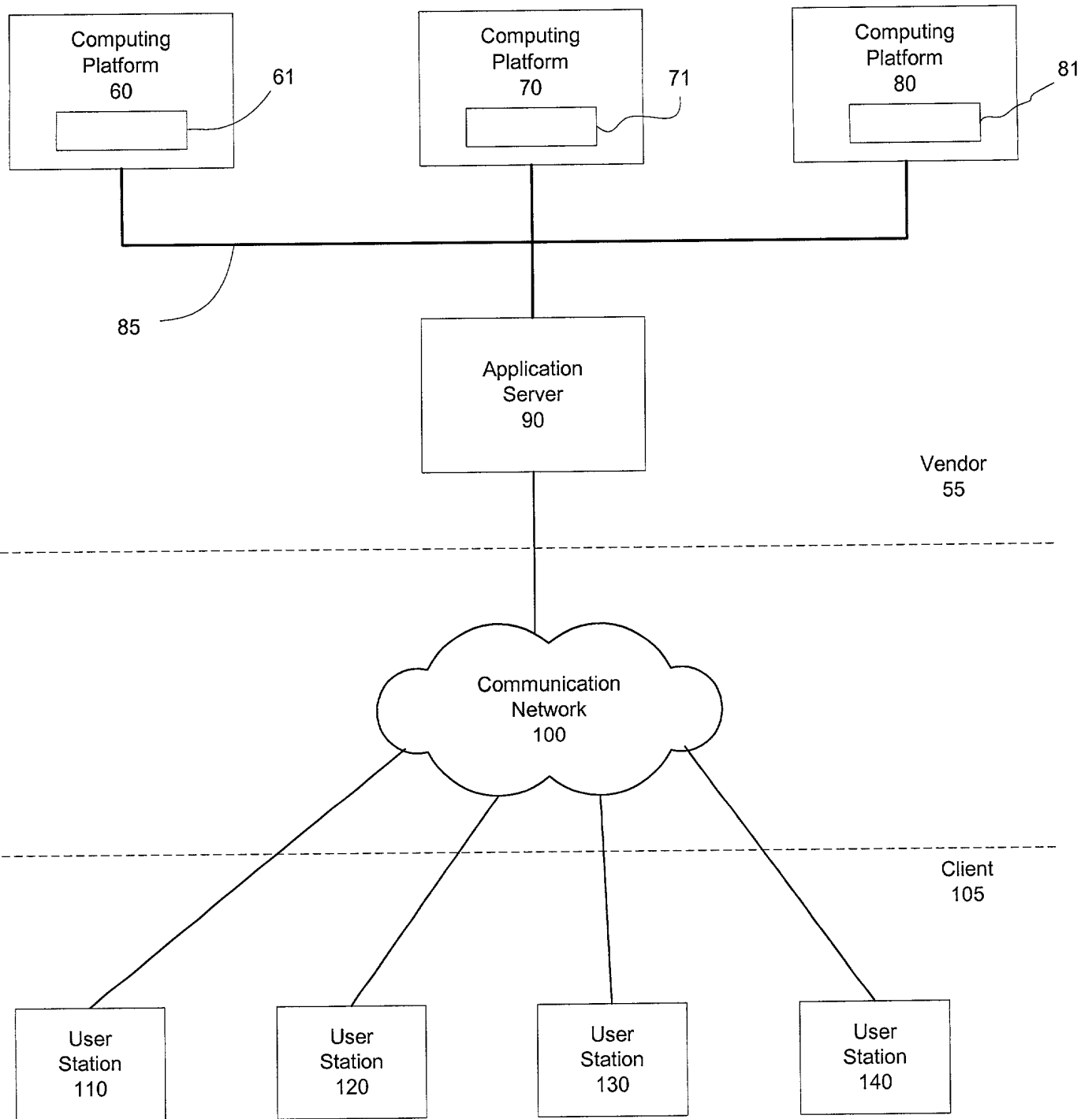


Fig. 2

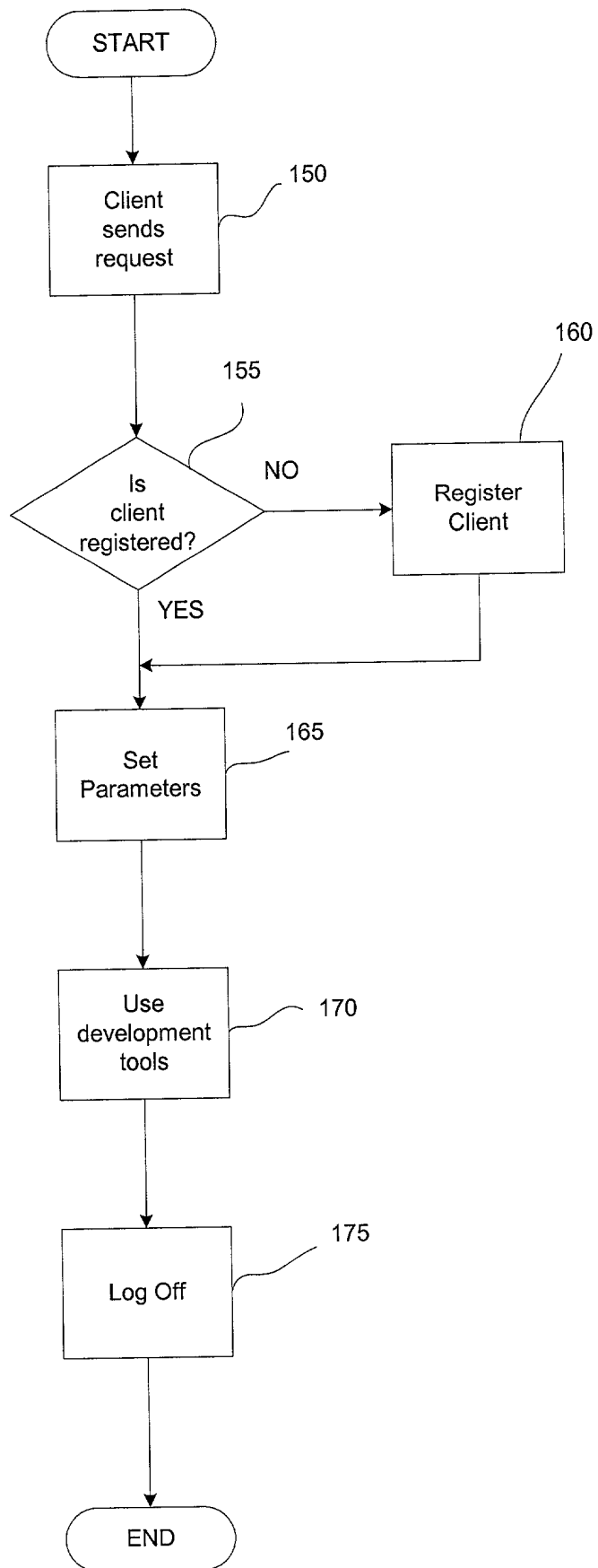


Fig. 3

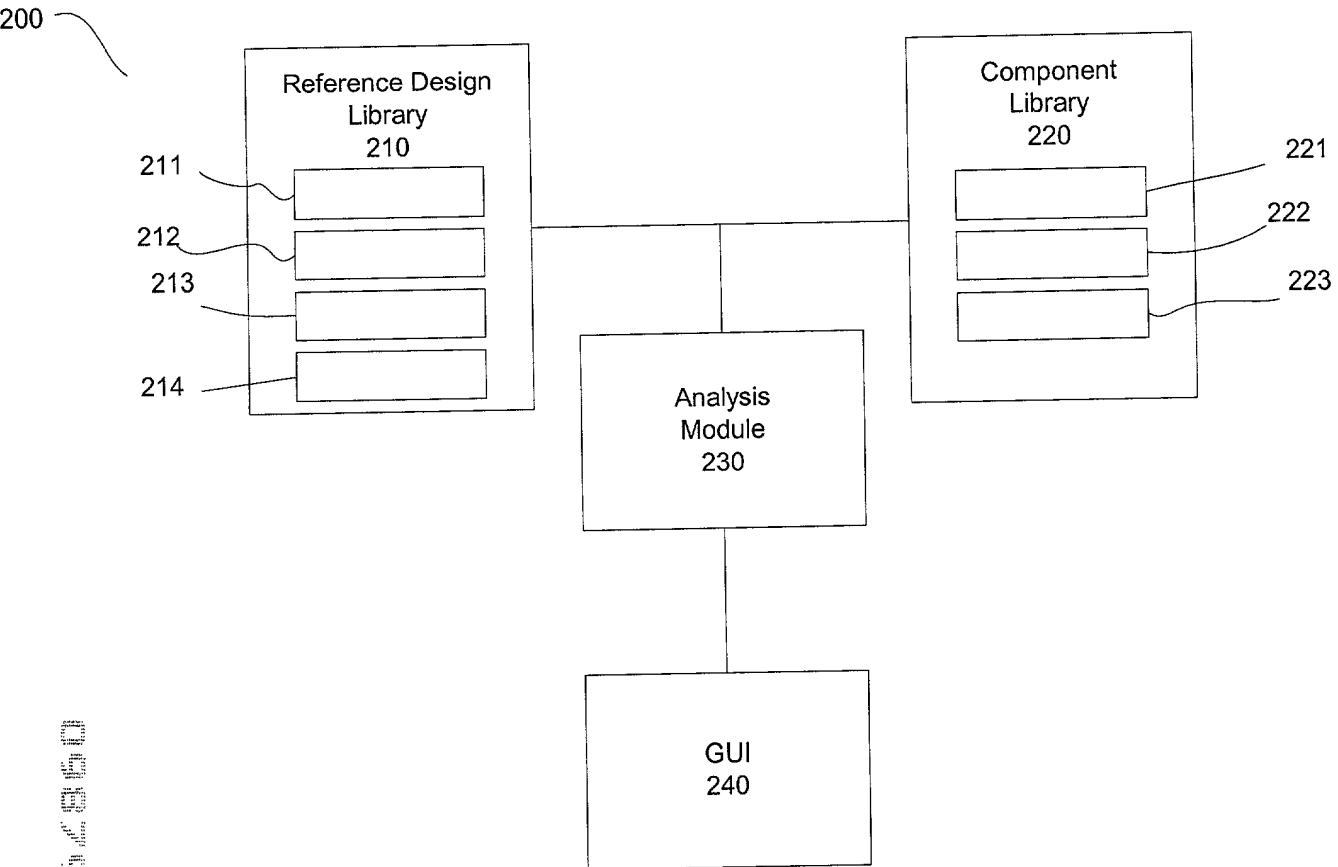


Fig. 4

FIG. 4

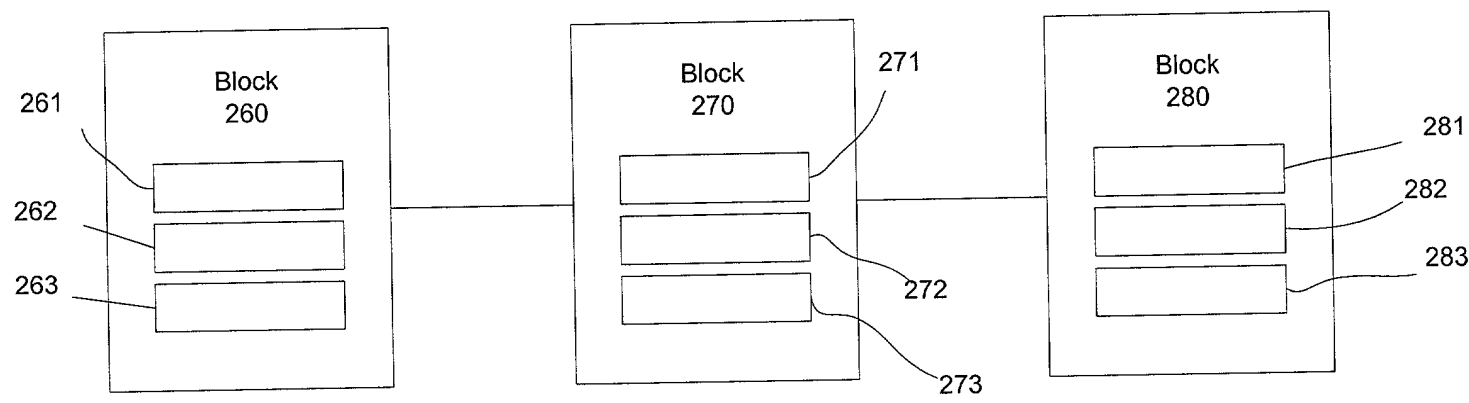


Fig. 5

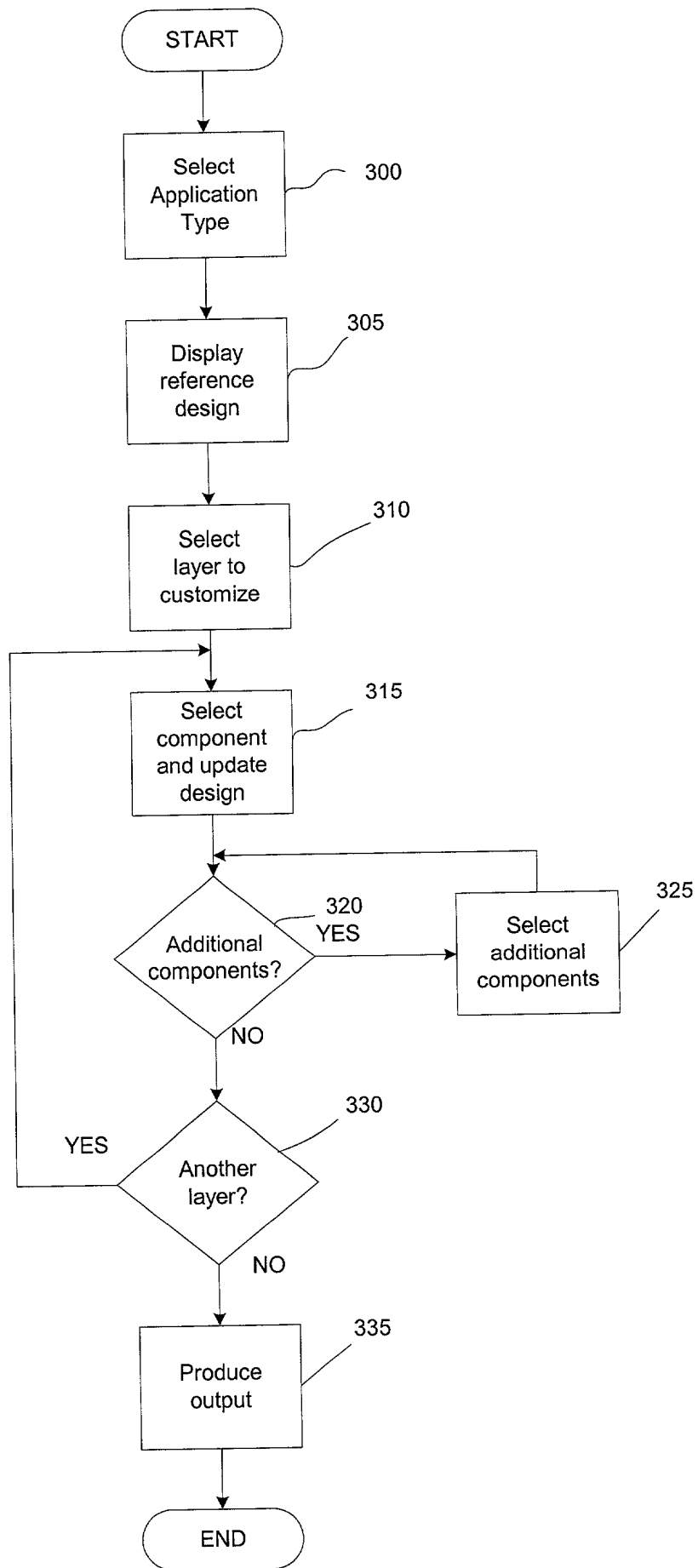


Fig. 6

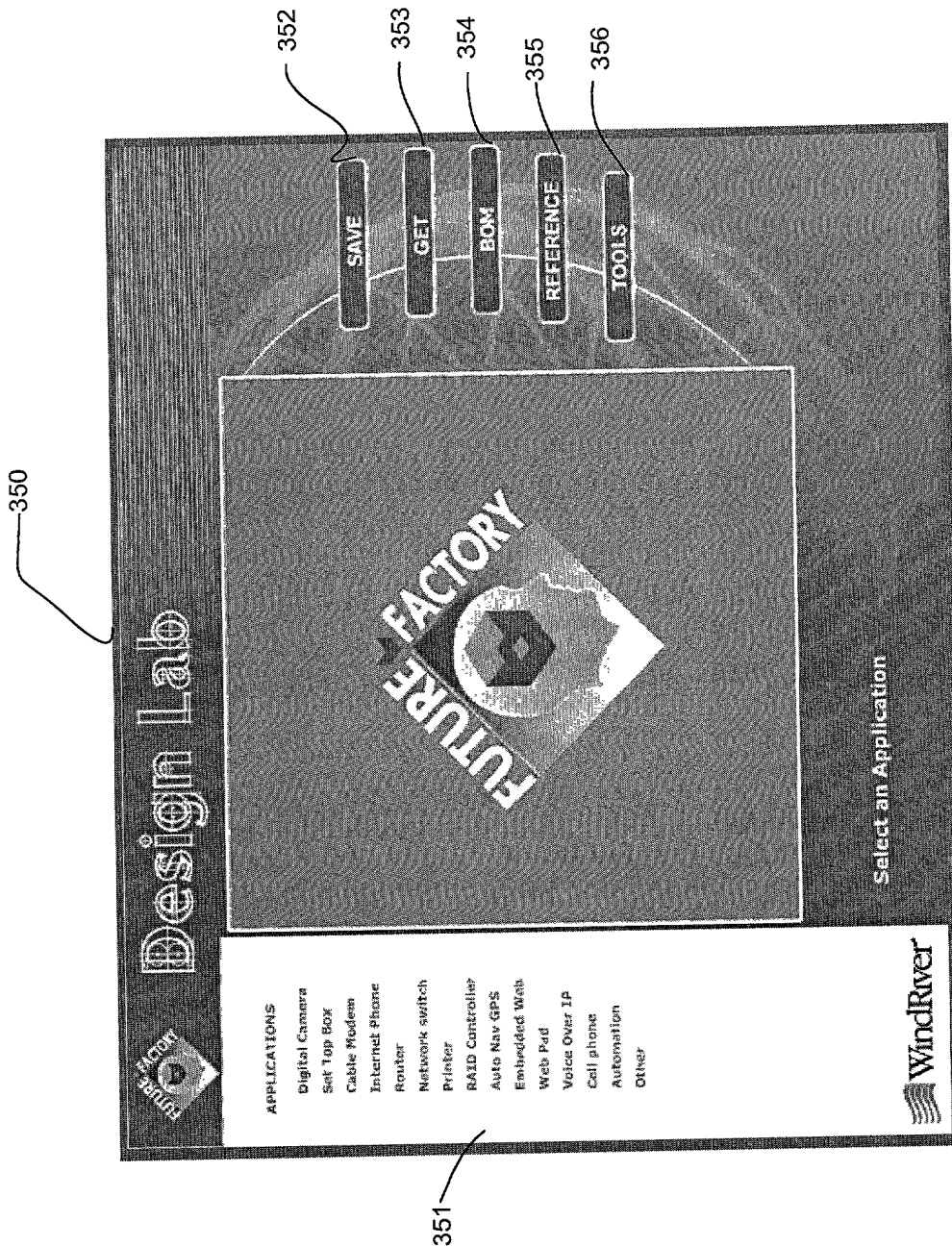


Fig. 7a

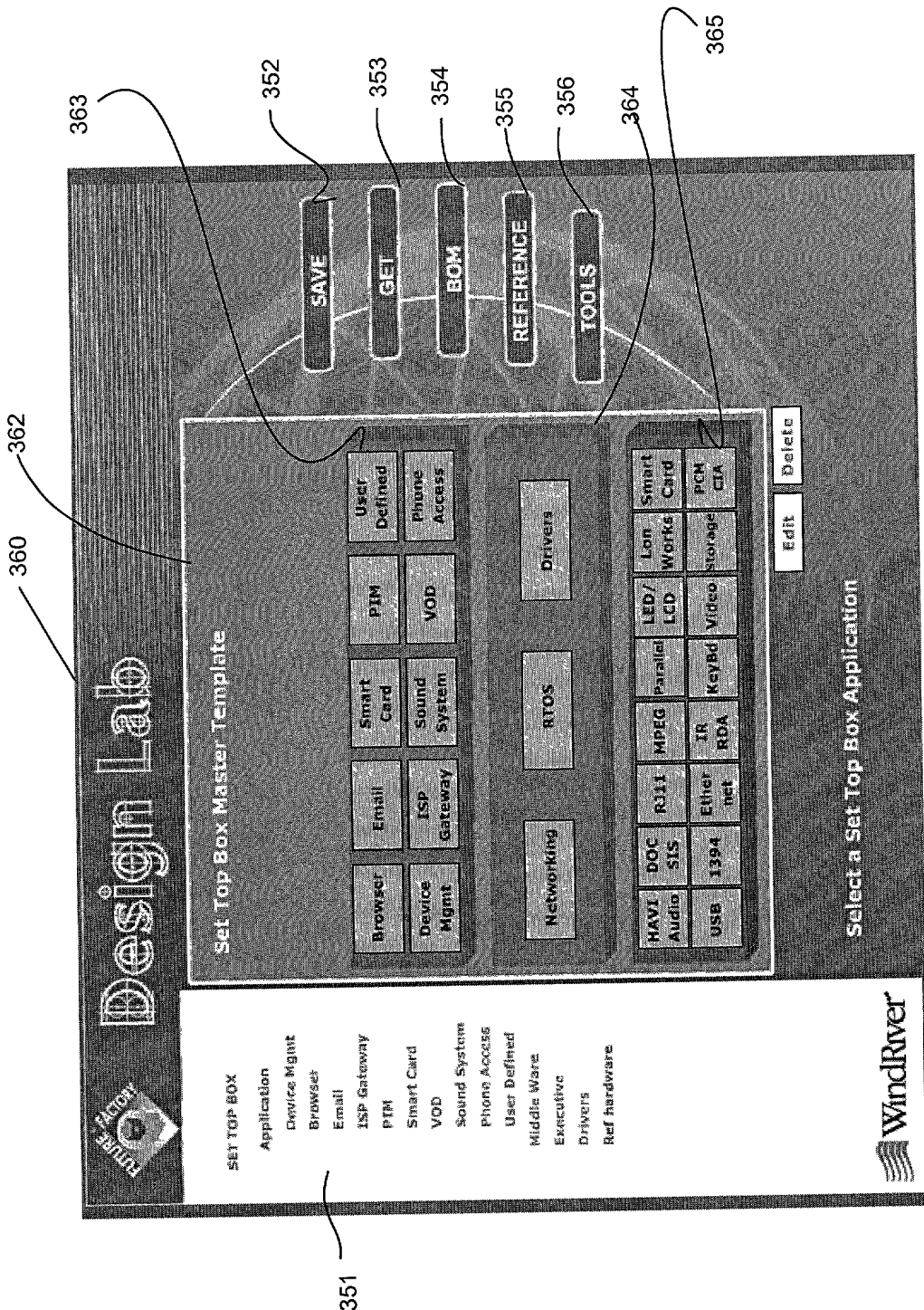


Fig. 7b

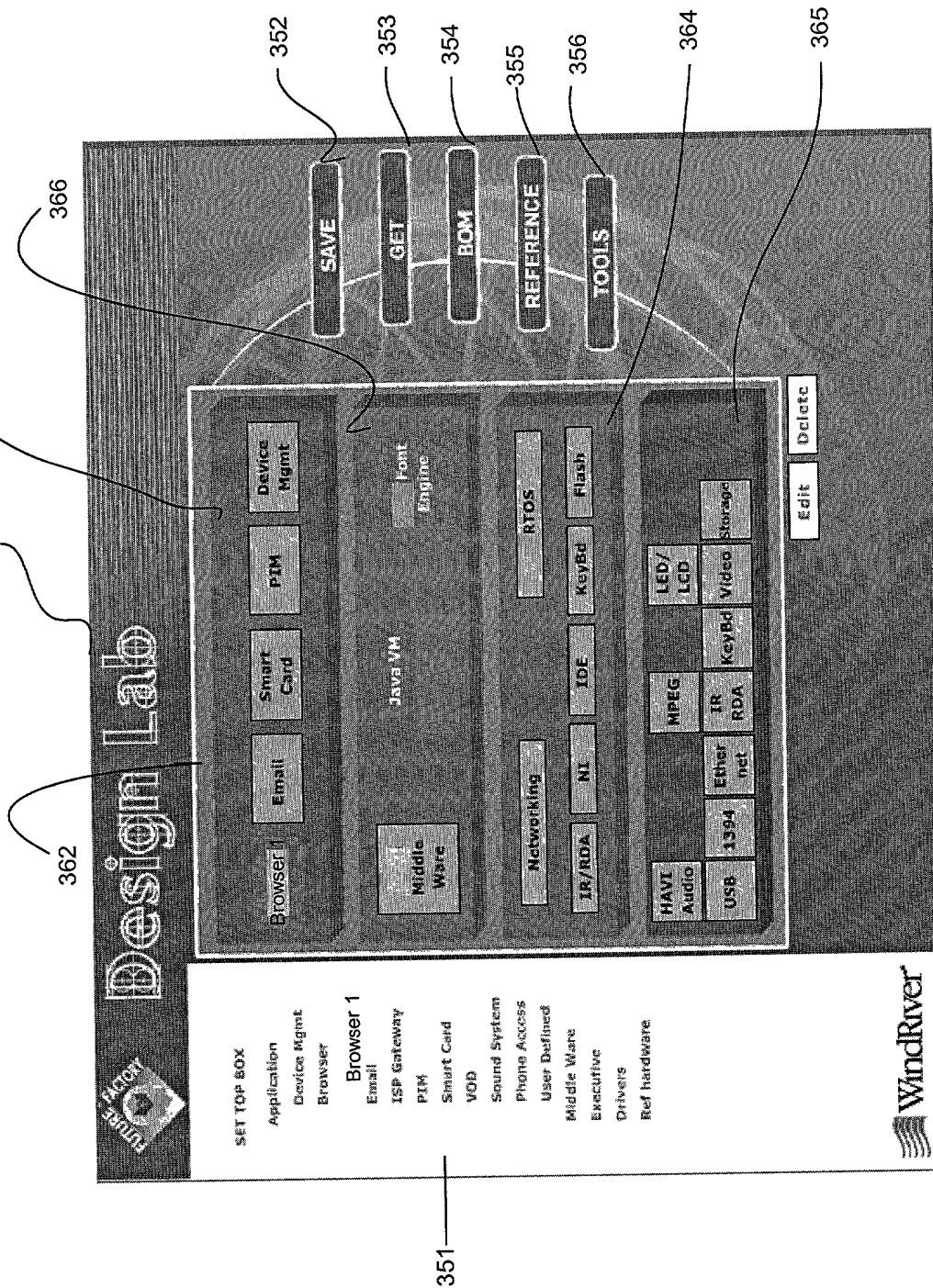


Fig. 7d

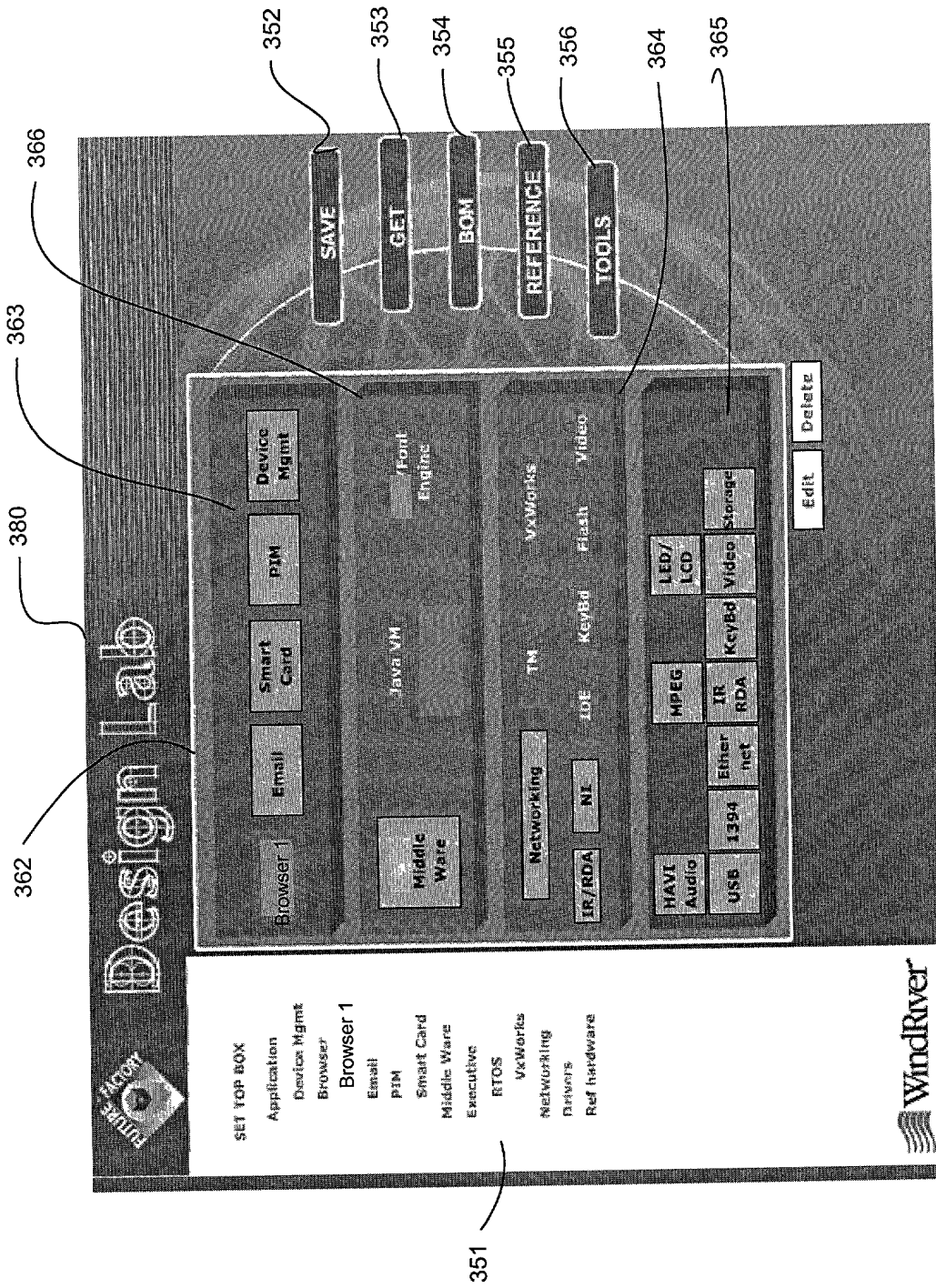


Fig. 7e

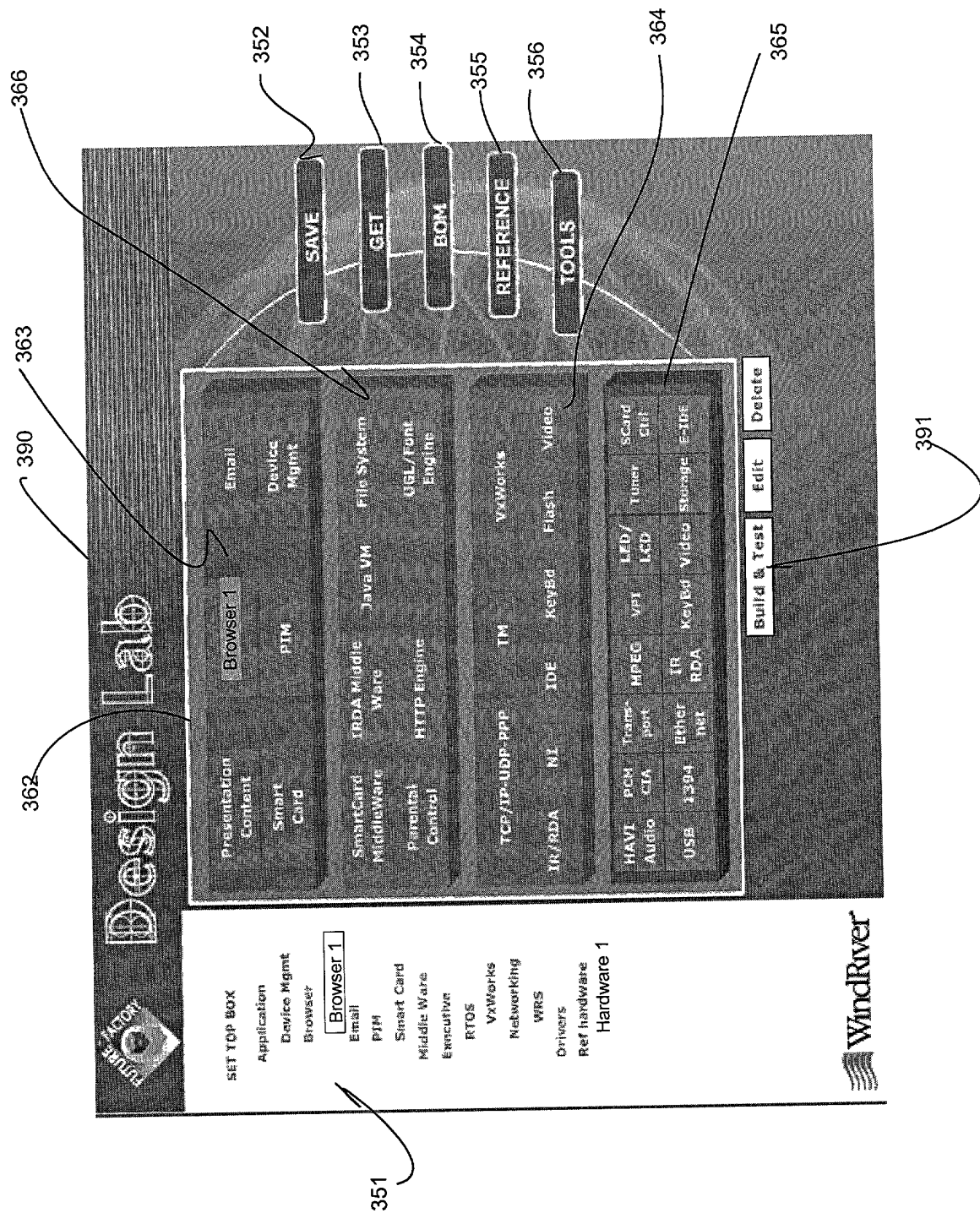


Fig. 7f

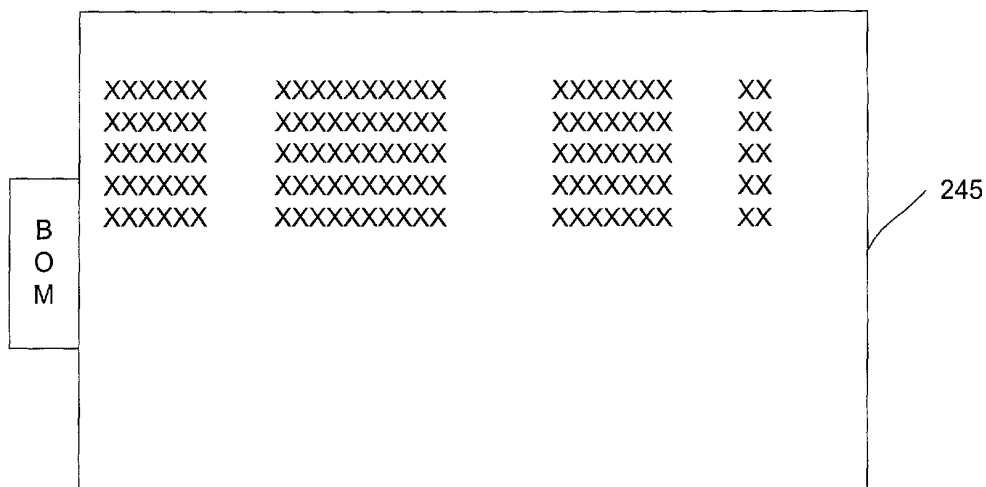


Fig. 8a

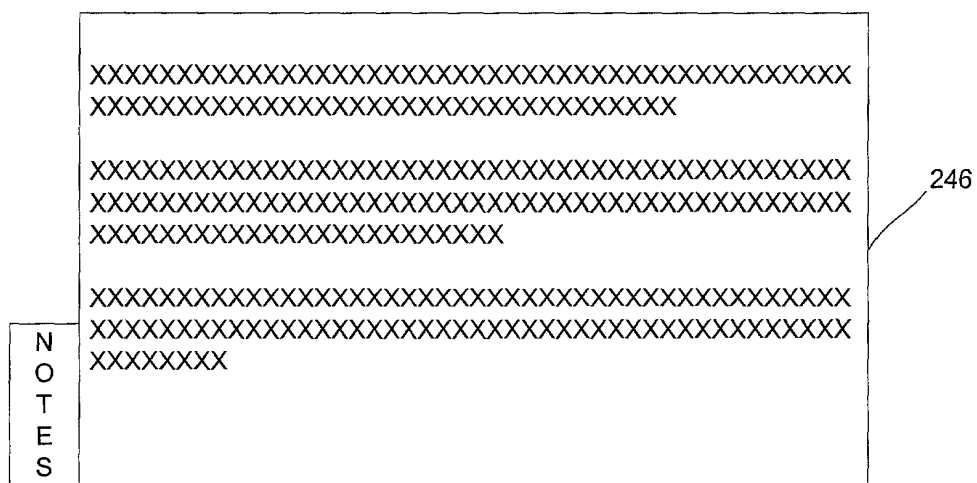


Fig. 8b

400

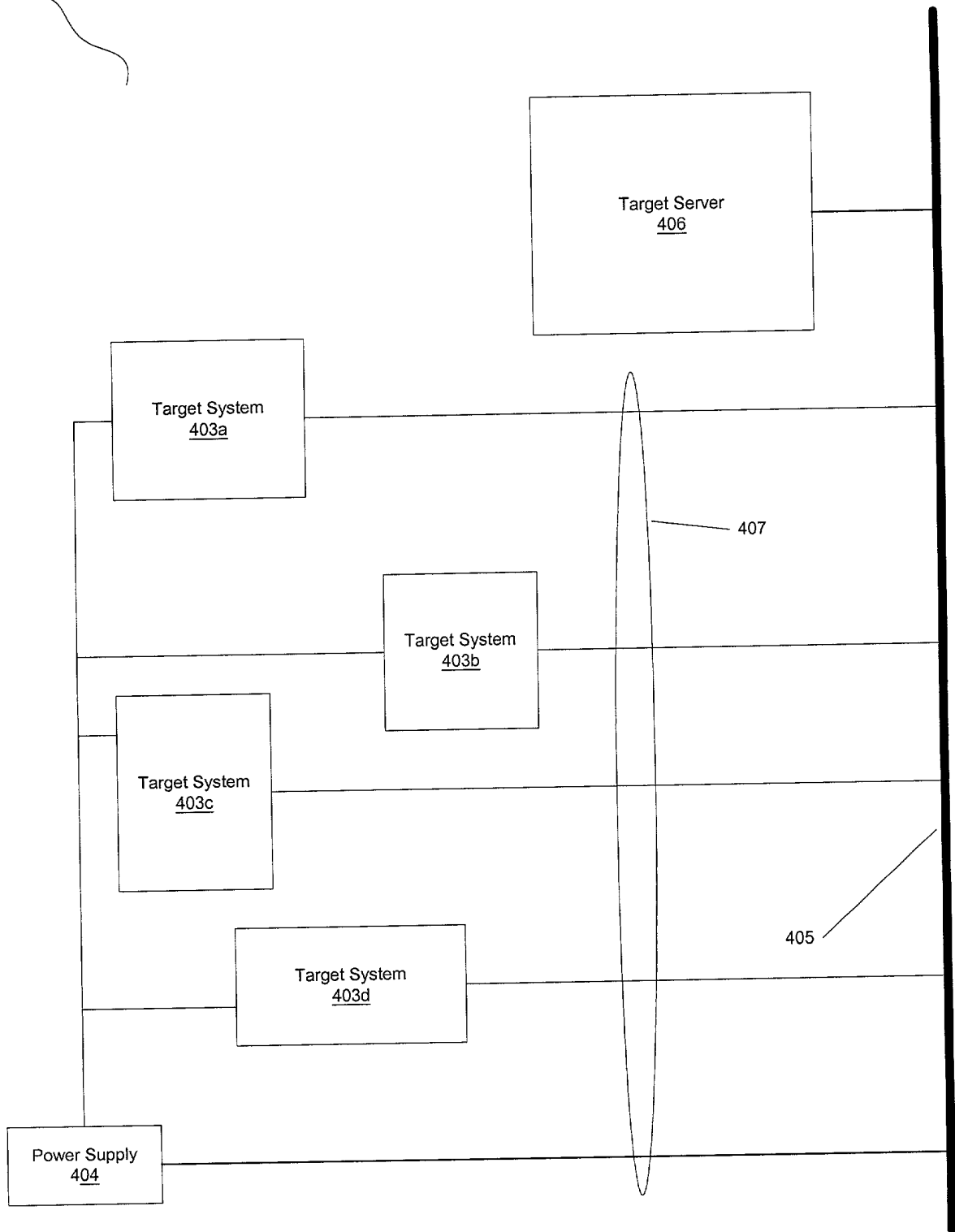


Fig. 9

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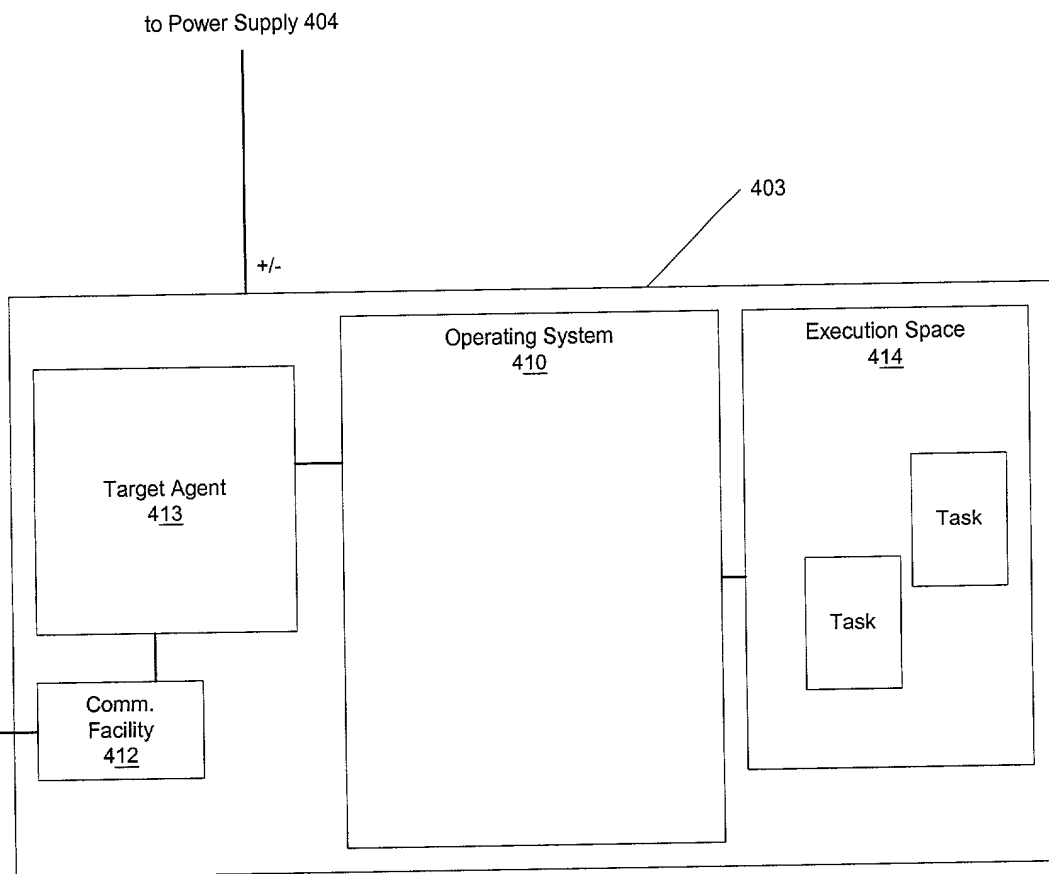


Fig. 10

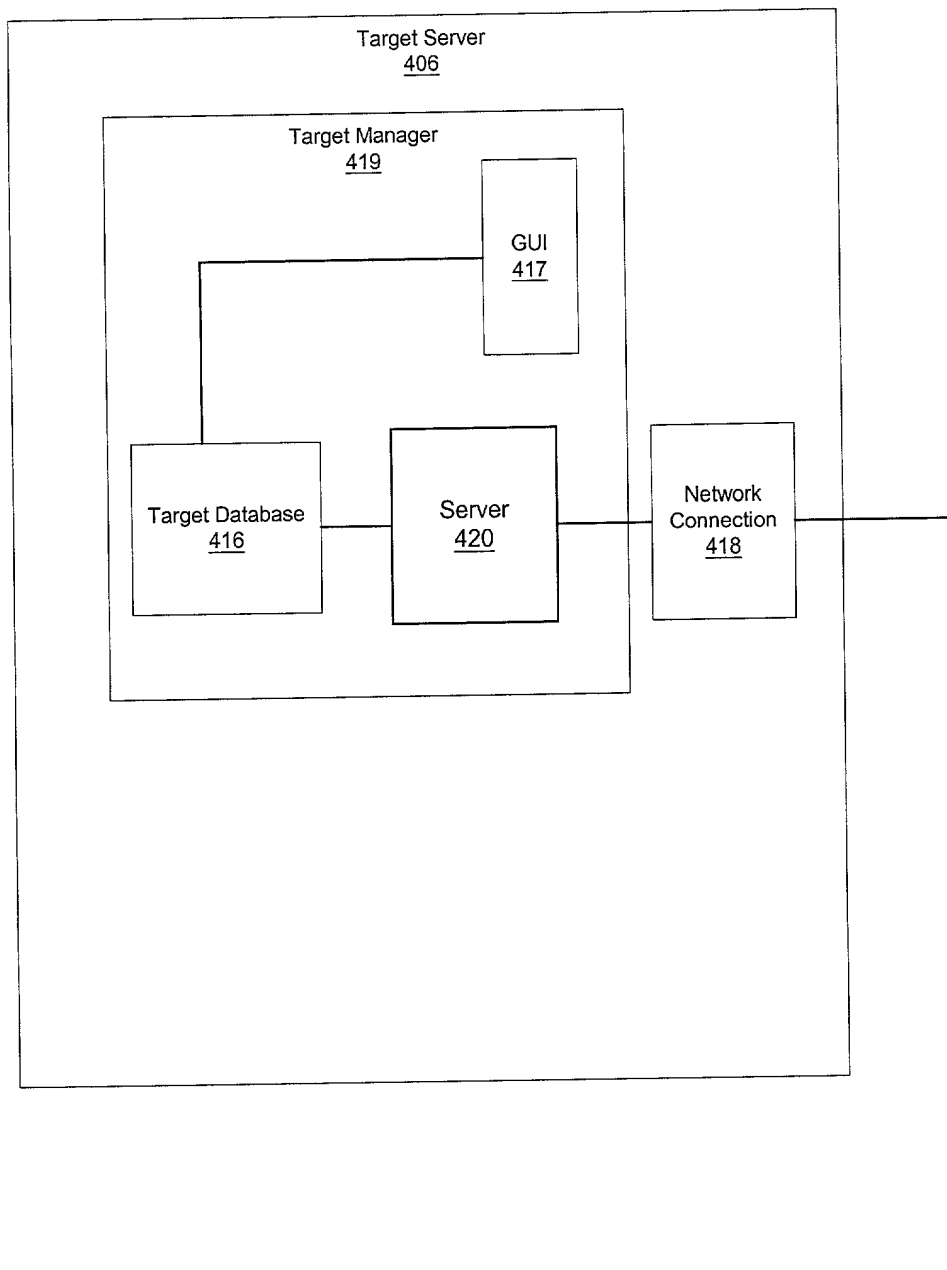


Figure 11

FIG. 12

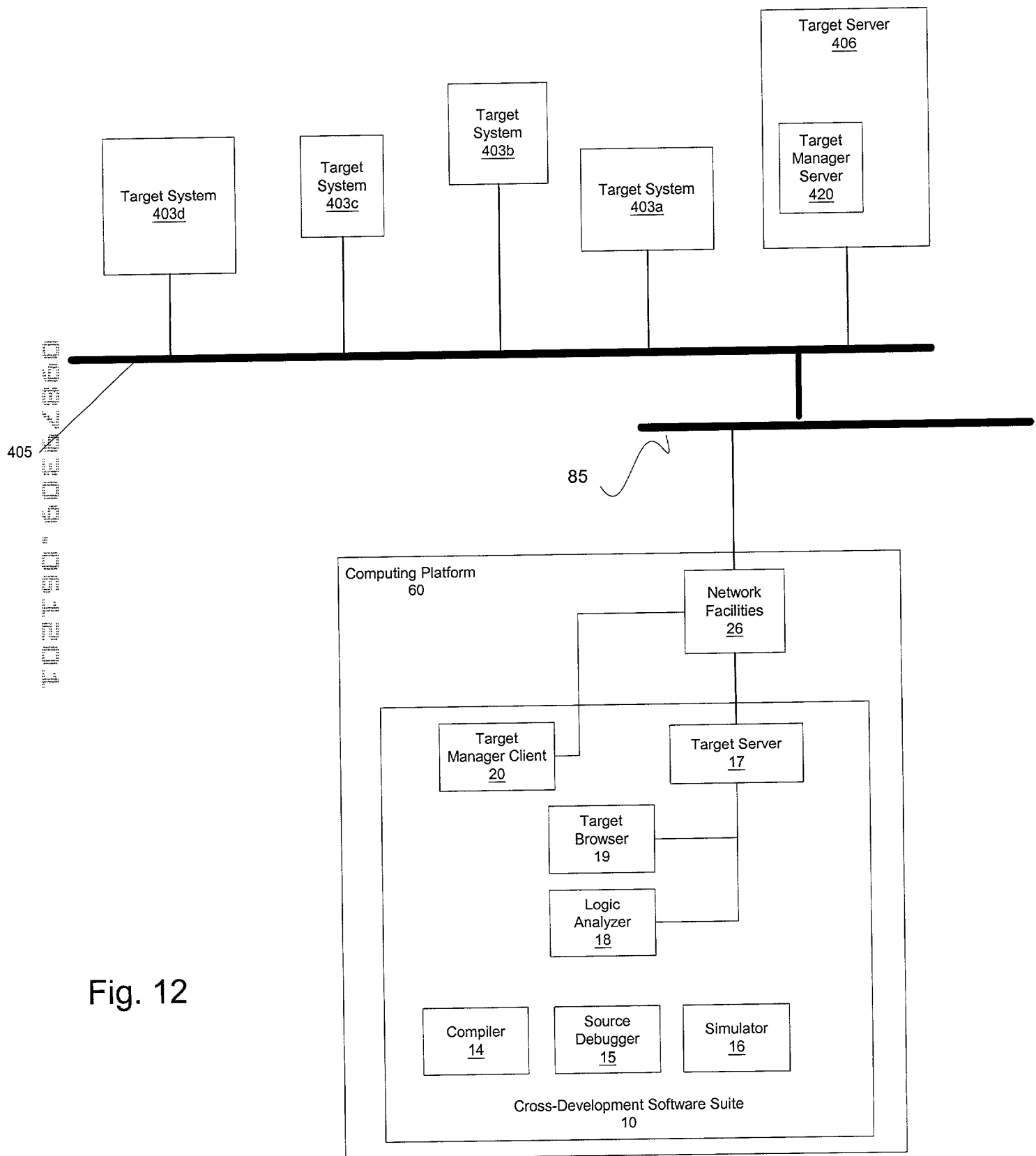


Fig. 12

22

FIG. 13

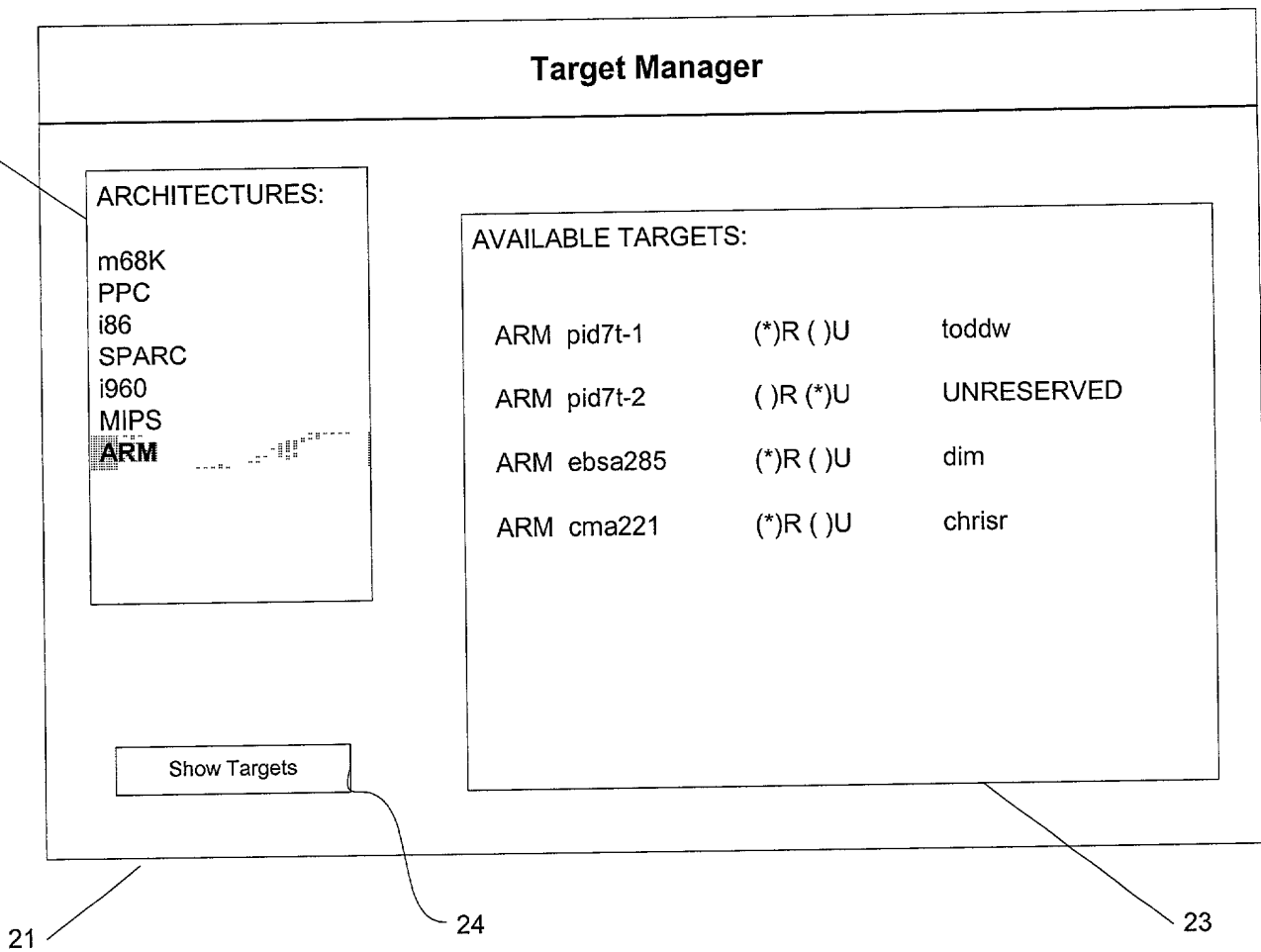


Fig. 13

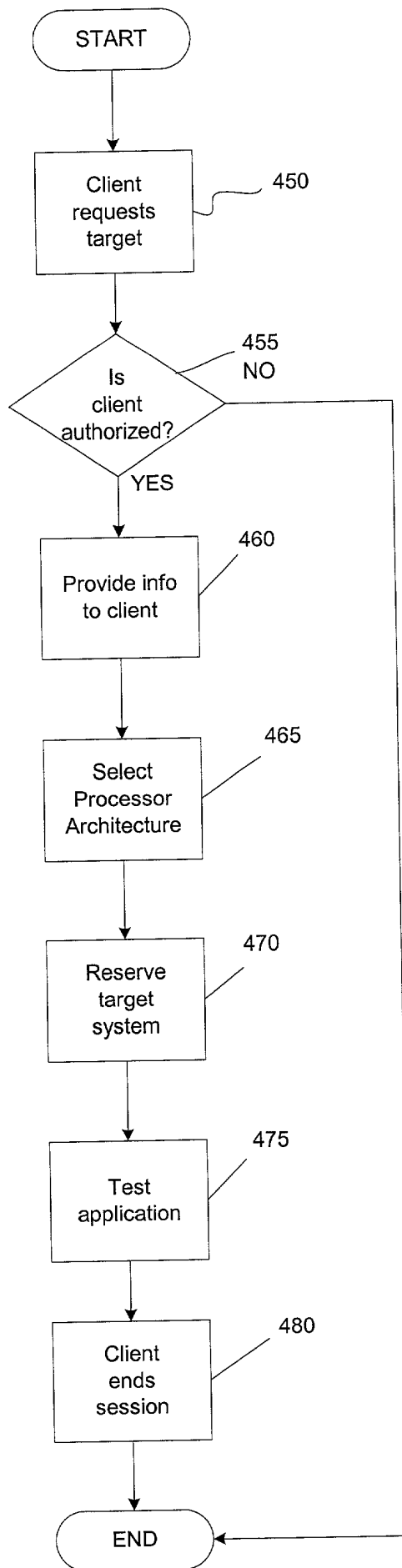


Fig. 14

Target Manager

ARCHITECTURES:

m68K
PPC
i86
SPARC
i960
MIPS
ARM

pid7t-1

all times GMT

00:00-01:00	()R ()U	UNRES	12:00-13:00	()R ()U	UNRES
01:00-02:00	()R ()U	UNRES	13:00-14:00	()R ()U	UNRES
02:00-03:00	()R ()U	UNRES	14:00-15:00	()R ()U	UNRES
03:00-04:00	()R ()U	UNRES	15:00-16:00	()R ()U	UNRES
04:00-05:00	()R ()U	UNRES	16:00-17:00	()R ()U	UNRES
05:00-06:00	()R ()U	UNRES	17:00-18:00	()R ()U	UNRES
06:00-07:00	()R ()U	UNRES	18:00-19:00	()R ()U	UNRES
07:00-08:00	()R ()U	UNRES	19:00-20:00	()R ()U	UNRES
08:00-09:00	()R ()U	UNRES	20:00-21:00	()R ()U	UNRES
09:00-10:00	()R ()U	UNRES	21:00-22:00	()R ()U	UNRES
10:00-11:00	()R ()U	UNRES	22:00-23:00	()R ()U	UNRES
11:00-12:00	()R ()U	UNRES	23:00-00:00	()R ()U	UNRES

Fig. 15